

3U VPX Rear IO Transition Module for VPX3-ZU1

Overview

The **RTM-ZU1-A** is an 3U OpenVPX Rear IO Transition Module for the VPX3-ZU1 3U OpenVPX Zynq Ultrascale+ module from PanaTeQ.

The following interfaces are available through the **Rear Panel** connectors:

- 2x Ethernet 100/1000BaseT RJ-45 connectors
- 2x USB 3.0 Type-A stacked connectors
- 2x USB 2.0 Type-A stacked connectors
- 1x mini Display Port 1.2 video output connector

Also these interfaces are available using the **on-board** vertical connectors.

- 1x SATA 3.1
- 1x micro USB UART BMC
- 20-pin for 20x GPIOs
- 10-pin 2x RS-232/422/485 + 2x CAN-Bus
- 14-pin standard Xilinx JTAG Prog/Debug
- Battery Socket VPX VBAT
- Push-Button VPX SysReset
- Main VPX Power Monitoring (VS1, VS2, VS3)

The air cooled PanaTeQ System Development Kit **VPX3-ZU1-PSDK-A** is available for the developers and includes a 4U lab chassis with 3/5 slots, the VPX3 -ZU1-B1N-AC and RTM-ZU1-A boards, a Linux BSP, the PanaTeQ FPGA Design Kit (PAN-FDK) and cables

Key Features

- 3U VPX RTM (100 x 81.5 mm)
- VITA 46.0, 46.10, 65.0 specifications compliant
- 0.8 or 1 Inch Rear Panel Width
- Dual 100/1000 Base-T RJ-45 with status Leds connectors
- Dual USB 3.0 Type-A stacked connectors
- Dual USB 2.0 Type-A stacked connectors
- Mini DisplayPort 1.2 Video Output connector
- SATA 3.1 on-board vertical connector
- Micro USB on-board vertical UART for VPX3-ZU1 BMC
- 10x LVDS / 20x GPIOs on-board 20-pin vertical connector
- 2x RS-232/422/CAN on-board 10-pin vertical connector
- Power Monitor (VS1, VS2, VS3) with status LEDs
- On-board Xilinx JTAG Programming/Debugging connector
- On-board Push-Button VPX SysReset
- On-board Socket for Battery sourcing VPX VBAT

Ordering Information

- RTM-ZU1-A : RTM for VPX3-ZU1 with 1inch Front-Panel
- RTM-ZU1-B : RTM for VPX3-ZU1 with 0.8inch Front-Panel

Contact Information

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