



# GLOBAL DESIGN SUMMITS

## Japan Summit

Monday, 26 October 2009 from 9:00 to 17:30

Hyatt Regency Tokyo Hotel, 2-7-2 Nishi-Shinjuku, Shinjuku-Ku,  
Tokyo, Japan 160-0023

Don't miss this **FREE**, full-day summit, especially designed for embedded developers!

- ✓ **Learn how to get better system performance, reliability and support for convergence with** the high-speed, flexible RapidIO interconnect and fabric
- ✓ **Talk tech with the experts** - understand how RapidIO technology provides the best solution to system-level interconnect issues
- ✓ **Discover the breadth of RapidIO products** –from IP to processors to DSPs to switches to software to full system solutions – all available to help build next generation systems

**Who Should Attend:** Engineering Managers • System Architects • Embedded Product Developers

### Agenda

- Expanded and enhanced technical sessions on RapidIO technology, applications examples, software considerations, practical design recommendations, RapidIO products and more! A light breakfast, lunch and snacks are included.
- Multi-vendor RapidIO interoperability demonstrations
- Meet the Expert sessions to get your RapidIO questions answered
- Discussion on RapidIO in today's designs
- ipod Nano and RapidIO Textbook Prize drawings at end of day

#### PARTICIPATING RAPIDIO TRADE ASSOCIATION MEMBERS

- ✓ Altera
- ✓ AMCC
- ✓ Freescale Semiconductor
- ✓ Integrated Device Technology (IDT)
- ✓ Texas Instruments
- ✓ ENEA
- ✓ Xilinx
- ✓ FET Corp
- ✓ CommAgility
- ✓ NAT

### Location

Tokyo Hyatt Regency

2-7-2 Nishi-Shinjuku, Shinjuku-Ku,  
Tokyo, Japan 160-0023

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<http://tokyo.regency.hyatt.com/hyatt/hotels/index.jsp>

**Space Is Limited – Register Before Oct. 9<sup>th</sup>**

[http://www.rapidio.org/events/design\\_summits\\_2009/](http://www.rapidio.org/events/design_summits_2009/)

or call 512.401.2900